	STM32L1	STM32L4
DAC	External trigger: • TIM6 TRGO • TIM7 TRGO • **TIM9 TRGO** • TIM2 TRGO • TIM4 TRGO • EXTI line9 • SW TRIG	External trigger: • TIM6 TRGO • **TIM8 TRGO** • TIM7 TRGO • **TIM5 TRGO** • TIM2 TRGO • TIM4 TRGO • EXTI line9 • SW TRIG
	typedef struct { __IO uint32_t CR; __IO uint32_t SWTRIGR; __IO uint32_t DHR12R1; __IO uint32_t DHR12L1; __IO uint32_t DHR8R1; __IO uint32_t DHR12R2; __IO uint32_t DHR12L2; __IO uint32_t DHR8R2; __IO uint32_t DHR12RD; __IO uint32_t DHR12LD; __IO uint32_t DHR8RD; __IO uint32_t DOR1; __IO uint32_t DOR2; __IO uint32_t SR; } DAC_TypeDef;	typedef struct { __IO uint32_t CR; __IO uint32_t SWTRIGR; __IO uint32_t DHR12R1; __IO uint32_t DHR12L1; __IO uint32_t DHR8R1; __IO uint32_t DHR12R2; __IO uint32_t DHR12L2; __IO uint32_t DHR8R2; __IO uint32_t DHR12RD; __IO uint32_t DHR12LD; __IO uint32_t DHR8RD; __IO uint32_t DOR1; __IO uint32_t DOR2; __IO uint32_t SR; __IO uint32_t **CCR**; __IO uint32_t **MCR**; __IO uint32_t **SHSR1**; __IO uint32_t **SHSR2**; __IO uint32_t **SHHR**; __IO uint32_t **SHRR**; } DAC_TypeDef;
	Reference Voltage: • External (2.0 V to VDDA, or 1.8 V to VDDA)	Reference Voltage: • external (2.0 V to VDDA) • internal (2.048 V or 2.5 V)